ATCA-3671 Getting Started

2025-03-20

Contents

ATCA Overview

This document describes how to install, configure, and test the ATCA-3671 FPGA Module for ATCA. The ATCA-3671 is programmable both through LabVIEW using LabVIEW FPGA and through BEEcube Platform Studio (BPS).

⚠ This icon denotes a caution, which advises you of precautions to take to avoid injury, data loss, or a system crash.

Caution Observe all instructions and cautions in the user documentation. Using the model in a manner not specified can damage the model and compromise the built-in safety protection. Return damaged models to NI for repair.

Attention Suivez toutes les instructions et respectez toutes les mises en garde de la documentation utilisateur. L'utilisation d'un modèle de toute autre façon que celle spécifiée risque de l'endommager et de compromettre la protection de sécurité intégrée. Renvoyez les modèles endommagés à NI pour réparation.

EMC Guidelines

The ATCA-3671 was tested and complies with the regulatory requirements and limits for electromagnetic compatibility (EMC) stated in the product specifications. These requirements and limits provide reasonable protection against harmful interference when the product is operated in the intended operational electromagnetic environment.

This product is intended for use in industrial locations. However, harmful interference

may occur in some installations, when the product is connected to a peripheral device or test object, or if the product is used in residential or commercial areas. To minimize interference with radio and television reception and prevent unacceptable performance degradation, install and use this product in strict accordance with the instructions in the product documentation.

Furthermore, any changes or modifications to the product not expressly approved by National Instruments could void your authority to operate it under your local regulatory rules.

Notice To ensure the specified EMC performance, operate this product only with shielded cables and accessories. Fiber optic cables connected to SFP+ and QSFP+ ports do not require shielding.

Note To ensure the specified EMC performance, the length of any cable connected to the Ethernet port and the Mini-SAS HD ports must be no longer than 30 m (100 ft.). The lengths of fiber optic cables connected to the SFP+ or QSFP+ ports are not controlled. The length of any cable connected to any other port must be no longer than 3 m (10 ft.).

Verifying the System Requirements

To use the NI-ATCA FPGA Modules instrument driver, your system must meet certain requirements.

Refer to the product readme, which is available online on the driver software download page or on the release notes page, for more information about minimum system requirements, recommended system, and supported application development environments (ADEs).

Related information:

- https://ni.com/docs/

Unpacking the Kit

Notice To prevent electrostatic discharge (ESD) from damaging the device, ground yourself using a grounding strap or by holding a grounded object, such as your computer chassis.

1. Touch the antistatic package to a metal part of the computer chassis.
2. Remove the device from the package and inspect the device for loose components or any other sign of damage.

   Notice Never touch the exposed pins of connectors.

   Note Do not install a device if it appears damaged in any way.
3. Unpack any other items and documentation from the kit.

Store the device in the antistatic package when the device is not in use.

Verifying the Kit Contents

- ATCA-3671 module
- Ethernet cable
- ***ATCA-3671 Getting Started Guide*** (this document)

Note Your ATCA-3671 kit may contain additional NI ATCA products.

Required Items

In addition to the kit contents, you must provide the following additional items:

- Single-Module ATCA Chassis[1][1]

  Note If your kit does not include a chassis, you can purchase one from ni.com or by contacting your local NI sales representative.
- Rear transition module (RTM) from the following list:

1. Included in some ATCA-3671 kits.

  - (Recommended) RTM-3661 PCI Express Rear Transition Module for ATCA[1]
  - RTM-3662 High-Speed Serial Rear Transition Module for ATCA

> **Note** The RTM-3662 is supported only in BPS. To access BPS-related resources and documentation for the RTM-3662 within the BPS Support Community, visit ni.com/info and enter the Info Code `BPSsupport`.

- DC power supply[1] as described in the device specifications
- Number 2 Phillips screwdriver
- Number 1 Phillips screwdriver
- Small flat-blade screwdriver
- Host PC or laptop, or PXIe-8394 Bus Extension Module

ATCA I/O Modules (Optional)

In addition to the required items, you may choose to provide one or more of the following ATCA I/O modules.

- AIO-3681 Digitizer Module for FMC, with the following components:
  - Riser card (x1)
  - Male-to-female M2.5 standoffs (10 mm hex length, 6 mm thread length) (x2)
  - Male-to-female M2.5 standoffs (12.5 mm hex length, 3.5 mm thread length) (x2)
  - M2.5x8 screws (x2)
  - M2.5x6 screws (x2)
- AIO-3682 Signal Generator Module for FMC, with the following components:
  - Riser card (x1)
  - Male-to-female M2.5 standoffs (x4)
  - M2.5x8 screws (x2)
  - M2.5x6 screws (x2)
- AIO-3691 High-Speed Serial Adapter Module [2], with the following components:
  - Female-to-female standoffs (x2)
  - M2.5x8 screws (x2)
  - M2.5x6 screws (x4)

2. Included in some ATCA-3671 kits.

Preparing the Environment

Ensure that the environment you are using the ATCA-3671 in meets the following specifications.

Ambient temperature range	0 °C to 40 °C
Operating temperature range	
Used with a Single-Module ATCA Chassis	0 °C to 25 °C
Used with a 14-Slot ATCA Chassis	Dependent on final system installation

Note Operating temperatures are valid only when the ATCA-3671 module is used with the specified chassis.

Operating humidity	10% to 90% RH, noncondensing
Pollution Degree	2
Maximum altitude	2,000 m at 25 °C ambient temperature

Indoor use only.

Notice Electrostatic Discharge (ESD) can damage the AIO-3681/3682 and AIO-3691. To prevent damage, use industry-standard ESD prevention measures during installation, maintenance, and operation.

Installing the Software

Note If you are using BPS, visit ni.com/info and enter the Info Code `BPSsupport` to access BPS-related resources within the BPS Support Community.

1. Install an ADE, such as LabVIEW.
2. If you chose to install LabVIEW in step 1, install LabVIEW FPGA.
3. Visit ni.com/info and enter the Info Code `atcadriver` to access the driver download page for the latest NI-ATCA FPGA Modules software.
4. Download the NI-ATCA FPGA Modules driver software.
5. Follow the instructions in the installation prompts.

   Note Windows users may see access and security messages during installation. Accept the prompts to complete the installation.

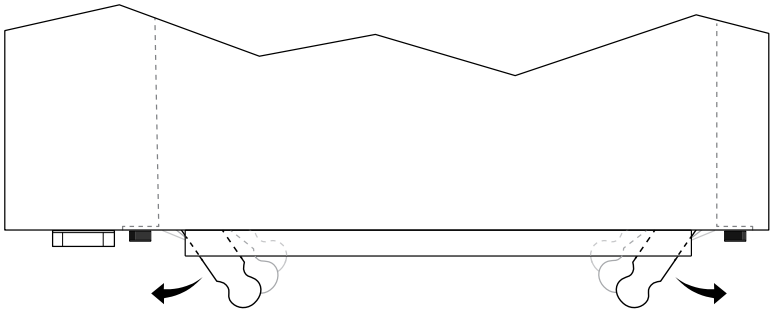
6. When the installer completes, select **Restart** in the dialog box that prompts you to restart, shut down, or restart later.

Installing the RTM

Notice Install the RTM before installing the ATCA-3671. Installing the ATCA-3671 before installing the RTM can damage the connectors of both modules.

1. Gently guide the module into the chassis module guides and slide the module into the chassis until it is fully inserted.
2. Close the module ejector handles to fully seat the module and latch it to the chassis, as shown in the following figure.

Figure 1. Using the Ejector Handles to Install the RTM



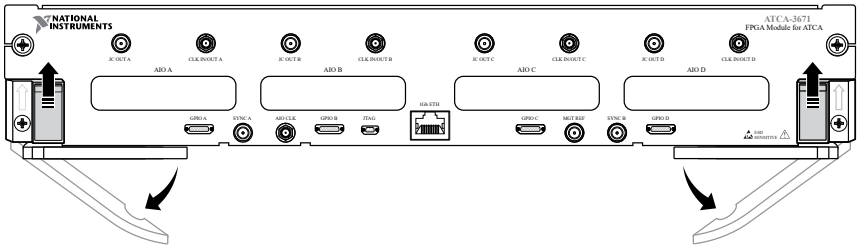
3. Tighten the four captive screws on the panel to secure the module.

Installing the ATCA-3671 Module

> **Notice** Install the RTM before installing the ATCA-3671. Installing the ATCA-3671 before installing the RTM can damage the connectors of both modules.

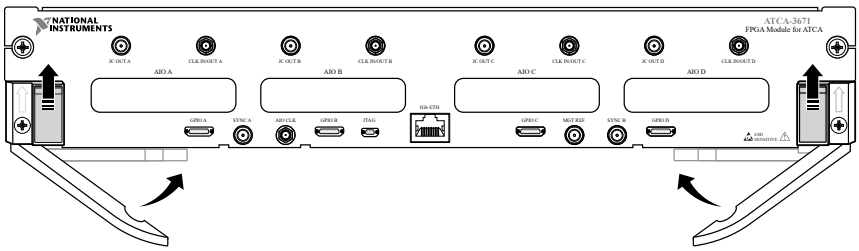
1. Ensure that the module ejector handles are in the open position, as shown in the following figure.

Figure 2. Opening the Module Ejector Handles



2. Gently guide the module into the chassis module guides and slide the module into the chassis until it is fully inserted.
3. Close the module ejector handles to fully seat the module and latch it to the chassis, as shown in the following figure.

Figure 3. Closing the Module Ejector Handles



4. Tighten the four captive screws on the panel to secure the module.

Installing AIO-3681/3682 Modules

Install one or more AIO-3681/3682 modules to enable analog-to-digital and digital-to-analog conversion of RF signals.

Determine how many modules you are installing and choose the appropriate number of empty slot(s) in the ATCA-3671. If hardware is present in the slot(s) you choose, remove the installed ATCA I/O module(s), riser card(s), and additional components from the slot(s).

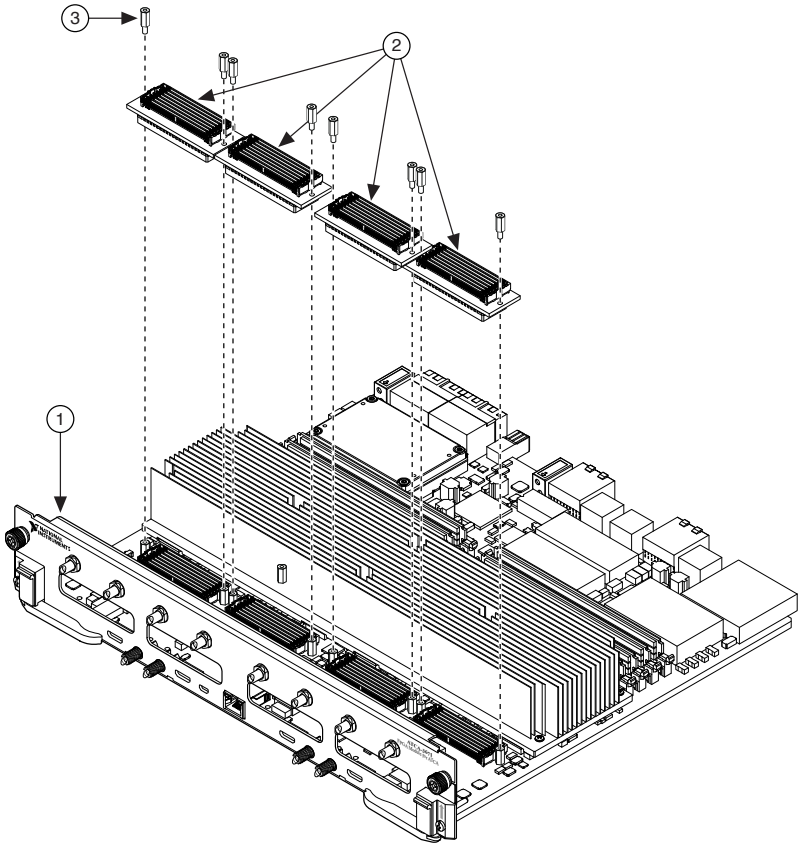
Ensure you have the following components for each AIO-3681/3682 module:

- Riser card (x1)
- Male-to-female M2.5 standoffs (10 mm hex length, 6 mm thread length) (x2)
- Male-to-female M2.5 standoffs (12.5 mm hex length, 3.5 mm thread length) (x2)
- M2.5x8 screws (x2)
- M2.5x6 screws (x2)

Complete the following steps to install one to four AIO-3681/3682 modules, depending on your chosen hardware configuration.

1. Install a riser card into the ATCA-3671 motherboard mating connector. Secure with two male-to-female M2.5 standoffs (10 mm hex length, 6 mm thread length), as shown in the following figure.

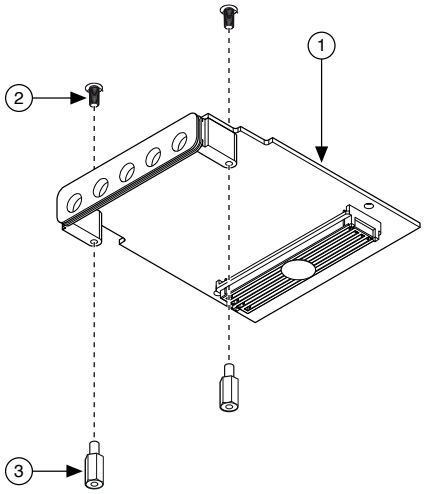
Figure 4. Installing Riser Card(s)



1. ATCA-3671 Module
2. Riser Cards
3. Male-to-Female M2.5 Standoff (10 mm Hex Length, 6 mm Thread Length)

2. Install two male-to-female standoffs into the AIO-3681/3682 module with two male-to-female M2.5 standoffs (12.5 mm hex length, 3.5 mm thread length), as shown in the following figure.

Figure 5. Installing Standoffs into the AIO-3681/3682 Module(s)



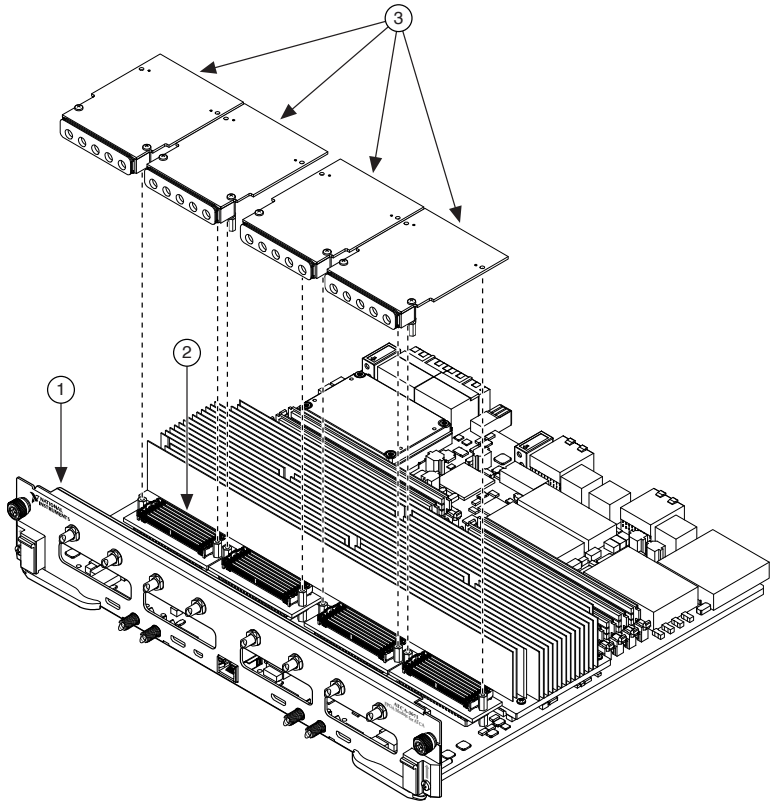
1. AIO-3681/3682 Module

2. M2.5x6 Screw, Preinstalled
3. Male-to-Female M2.5 Standoff (12.5 mm Hex Length, 3.5 mm Thread Length)

3. Align the AIO-3681/3682 module with the riser card connector and gently mate the connectors, as shown in the following figure.

> **Note** The AIO-3681/3682 module must be centered on the riser card connector so that the holes for the screws are aligned.

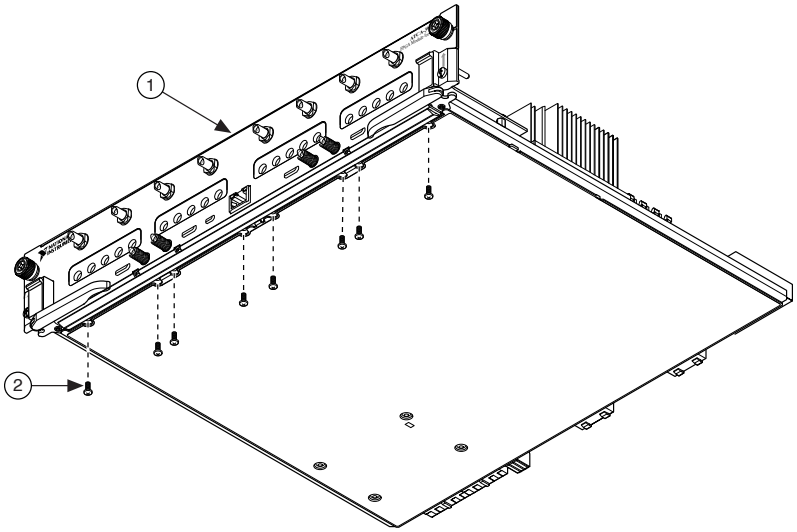
Figure 6. Aligning the AIO-3681/3682 Module(s) with the Riser Card(s)



1. ATCA-3671 Module
2. Riser Card
3. AIO-3681/3682 Modules

4. Secure the AIO-3681/3682 module to the ATCA-3671 module with two M2.5x8 screws from the underside of the ATCA-3671 module, as shown in the following figure.

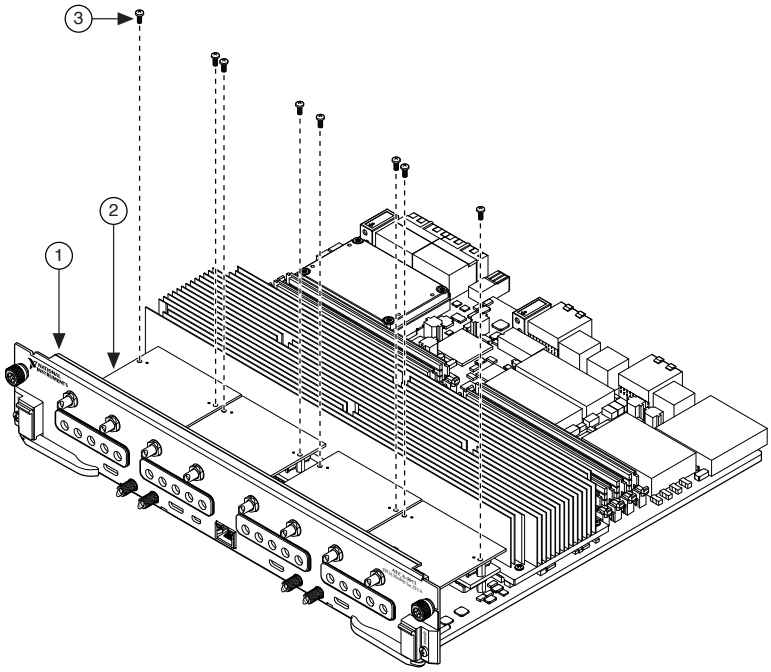
Figure 7. Securing the AIO-3681/3682 Module(s) to the ATCA-3671 Module and Riser Card(s)



1. ATCA-3671 Module
2. M2.5x8 Screw

5. Secure the AIO-3681/3682 module to the riser card with two M2.5x6 screws into the riser card standoffs, as shown in the following figure.

Figure 8. Securing the AIO-3681/3682 Module(s) to the Riser Card(s)



1. ATCA-3671 Module
2. AIO-3681/3682 Module
3. M2.5x6 Screw

Related tasks:

- Removing the ATCA I/O Module

Installing AIO-3691 Modules

Install one or more AIO-3691 modules to enable high-speed serial connectivity between each FPGA and the ATCA-3671.

Determine how many modules you are installing and choose the appropriate number of empty slot(s) in the ATCA-3671. If hardware is present in the slot(s) you choose, remove the installed ATCA I/O module(s), riser card(s), and additional components from the slot(s).

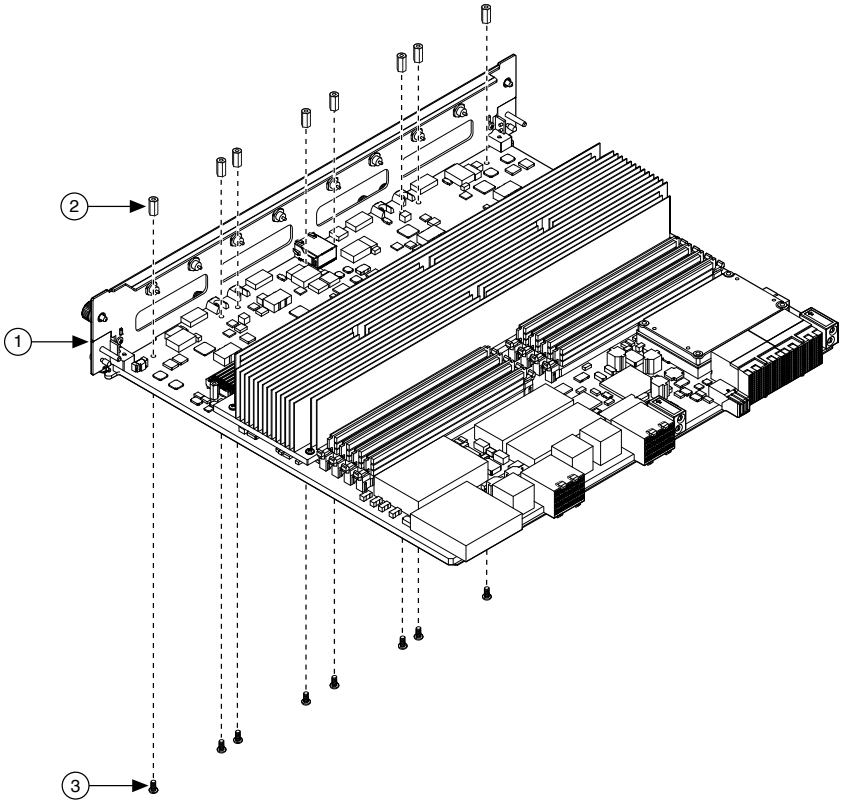
Ensure you have the following components for each AIO-3691 module:

- Female-to-female standoffs (x2)
- M2.5x8 screws (x2)
- M2.5x6 screws (x4)

Complete the following steps to install one to four AIO-3691 modules, depending on your chosen hardware configuration.

1. Install two female-to-female standoffs in the ATCA-3671 module with two M2.5x8 screws installed from the underside of the ATCA-3671 module as shown in the following figure.

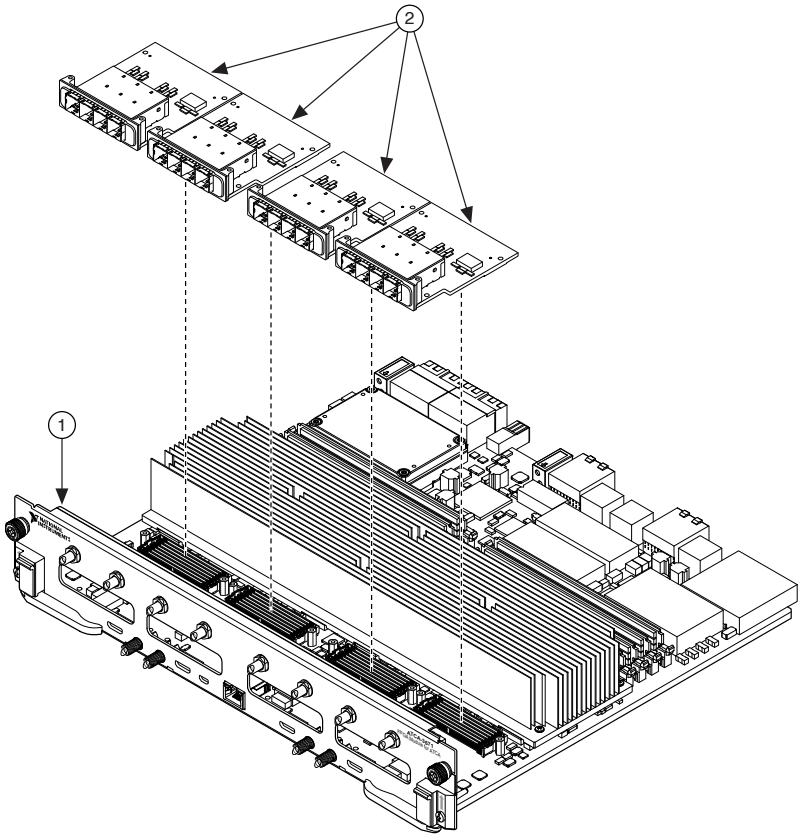
Figure 9. Installing Standoffs



1. ATCA-3671 Module
2. Female-to-female Standoff
3. M2.5x8 Screw

2. Align and install the FMC+ connector of the AIO-3691 module to the ATCA-3671 motherboard, as shown in the following figure.

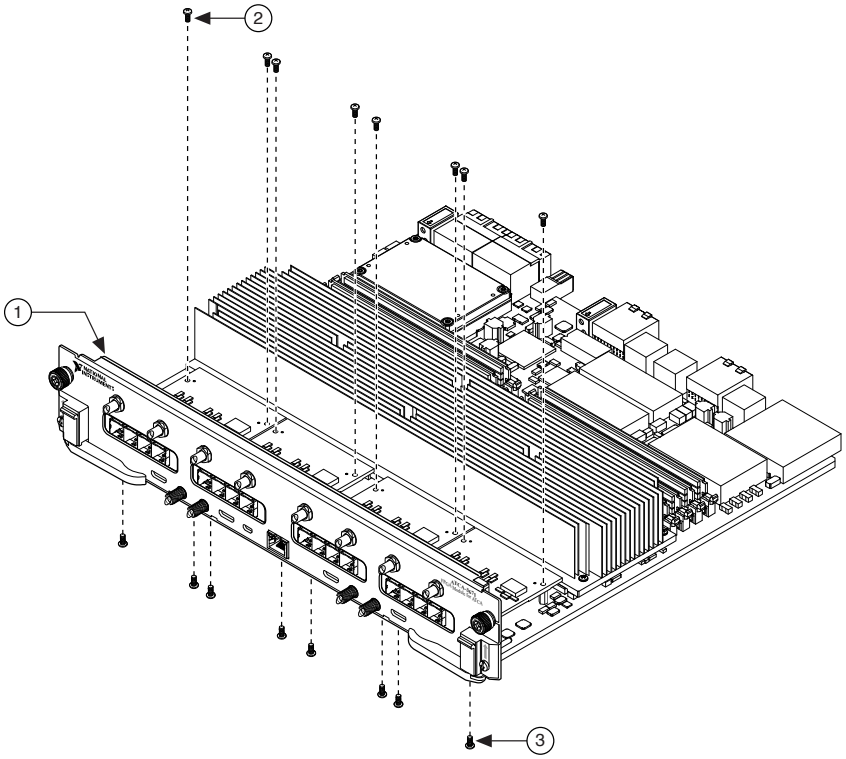
Figure 10. Aligning the AIO-3691 Module(s)



1. ATCA-3671 Module
2. AIO-3691 Modules

3. Secure the AIO-3691 module with two M2.5x6 screws installed from the underside of the ATCA-3671 module and two M2.5x6 screws installed from the top of the AIO-3691 module, as shown in the following figure.

Figure 11. Securing the AIO-3691 Module(s)



1. ATCA-3671 Module
2. M2.5x6 Screw
3. M2.5x6 Screw

Related tasks:

- Removing the ATCA I/O Module

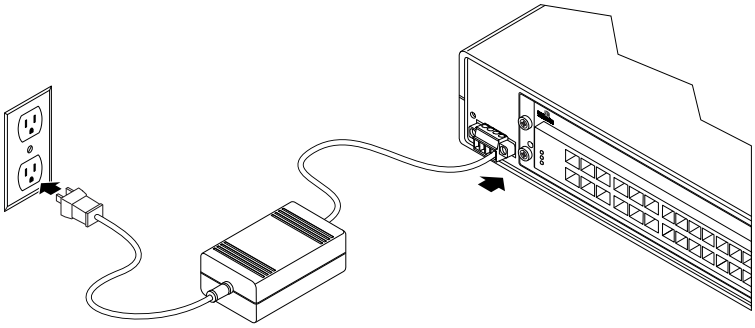
Powering On the ATCA-3671 Module

Complete the following steps to connect a power supply to the ATCA-3671.

Note The ATCA-3671 requires a -48 V external power supply that meets the product specifications.

1. Ensure the power source is turned off.
2. Connect the DC power supply to the ATCA-3671 back panel, as shown in the following figure.

Figure 12. Connecting the ATCA-3671 to Power (RTM-3662 Shown)



The ATCA-3671 powers on automatically once the DC power supply is connected to the power source.

3. Observe the colored LEDs on the front panel of the ATCA-3671 to check the device status, as described in the following table.

Table 1. LED State while Powering on the ATCA-3671

Color	State	Indication	Action
Blue	Off	The module is ready for use.	—
Green	Solid	The module is ready for use.	If you are using a host computer, wait for the green LED to be illuminated for 90 seconds before you start the host.
Blue	Solid	The module is not fully powered on.	Check that the ATCA-3671 is securely inserted in the chassis and that both ejector handles are fully latched.
Green	Off or blinking		

Related reference:

- ATCA-3671 Front Panel

Maximum Power Requirements

Note Power requirements are dependent on the adapter modules installed

and the contents of the FPGA application.

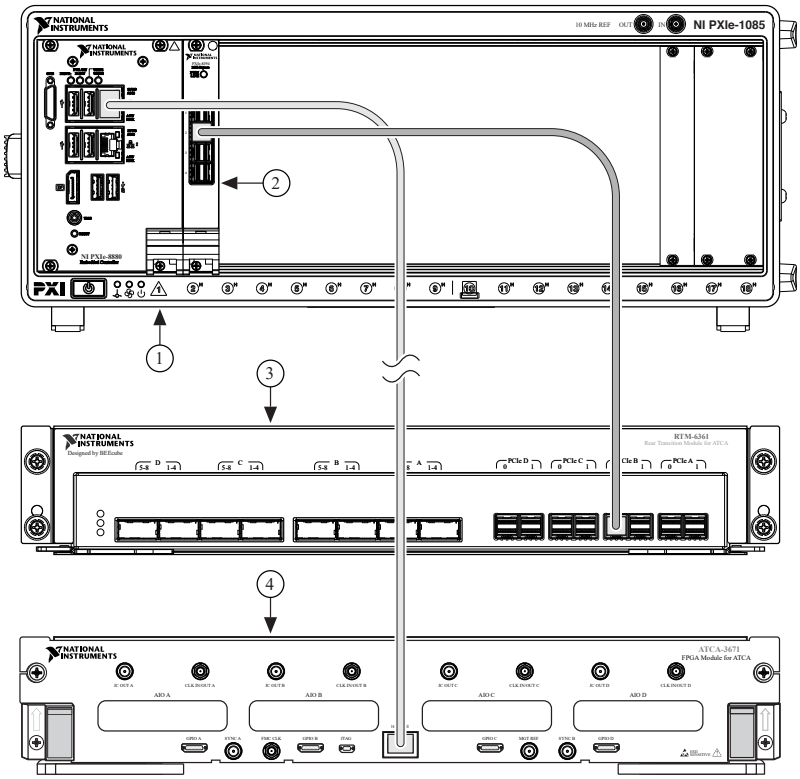
Power supply	-48 V
Current	9 A

Connecting to a Host Computer or PXI Chassis

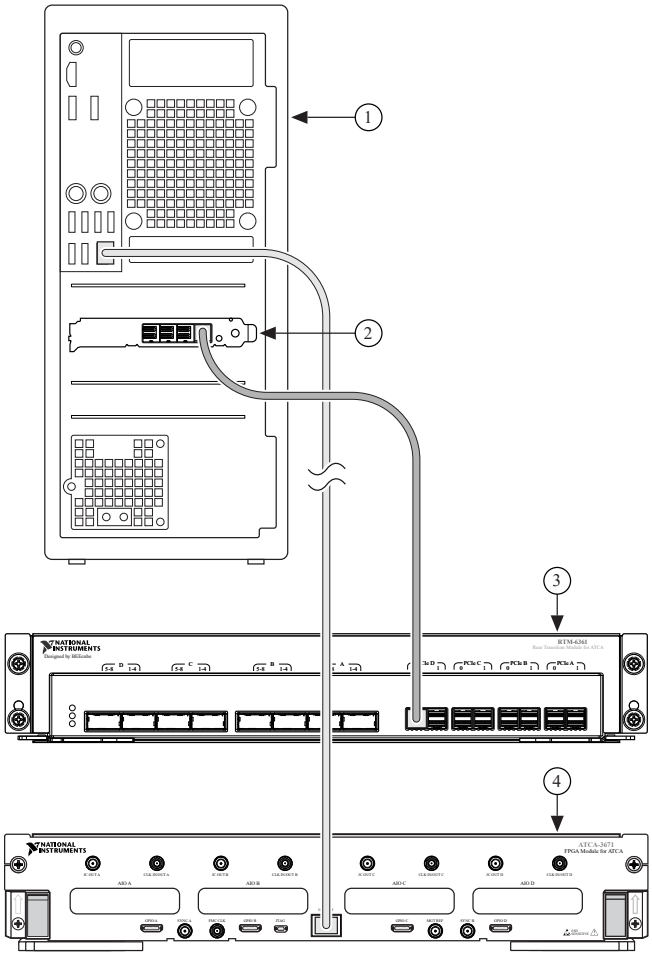
All modules must be properly installed within the ATCA chassis before you connect to a host computer or PXI chassis.

Note Ensure that the ATCA-3671 is powered on and has finished initializing before connecting the module to the host computer or PXI chassis.

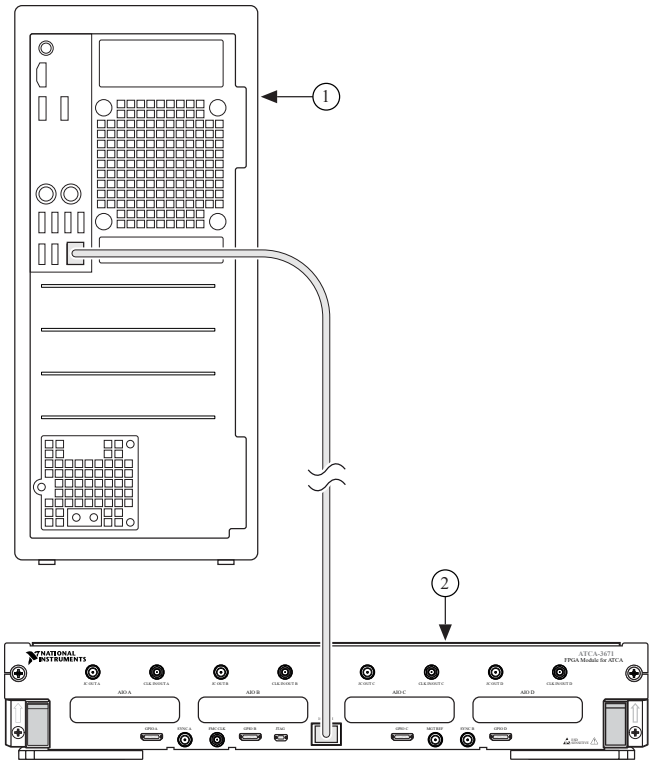
The following three figures show examples of hardware configuration options.



1. PXIe-1085 Chassis
2. PXIe-8394 Module
3. RTM-3661 Module
4. ATCA-3671 Module



1. Host PC
2. PXIe-8398 Remote Control Module
3. RTM-3661 Module
4. ATCA-3671 Module



1. Host PC
2. ATCA-3671 Module

Note To debug using the JTAG Programming Bundle for ATCA, set up any of the previous hardware configurations and connect the ATCA JTAG port to a PC using the adapter included in the JTAG Programming Bundle for ATCA.

Related tasks:

- Installing the Software
- Powering On the ATCA-3671 Module

Installing MXI Cables

Note You must use an RTM-3661 module with the ATCA-3671 module for host access to the FPGAs from LabVIEW. Direct host access is not supported for other RTMs.

1. Connect one end of a Gen3 MXI cable to the corresponding port on the installed

RTM-3661 module for each FPGA.

a. Start with Port 1 when you connect the MXI card. Connect MXI Port 1 to the first PCIe port (0, 1, ...) on the RTM that corresponds to the first FPGA (A, B, C, or D) to which you will connect.
b. Continue making connections in ascending order until you have made all necessary connections.
   For example, if you require two connections to FPGA B and one connection to FPGA C, first connect Port 1 on the MXI Card to PCIe B, Port 0 on the RTM. Then connect Port 2 on the MXI Card to PCIe B, Port 1 on the RTM. Finally, connect Port 3 on the MXI Card to PCIe C, Port 0 on the RTM.

2. Connect the other end of the Gen3 MXI cable to a compatible Gen3 MXI PCI Express or PXI Express adapter on the host computer or PXI chassis.

   Refer to the Gen3 MXI documentation for any additional connectivity requirements.

3. Power on the ATCA-3671 after all desired MXI connections are made between the RTM-3661 and host-side adapters.
4. Wait 90 seconds and then power on the host computer or PXI controller.

   Some host-side MXI adapters, such as the PXIe-8394 module, have a link status LED to indicate that a PCI Express link has been established successfully between the target and host. Refer to the Gen3 MXI x8 documentation to verify whether a link status LED may be used to verify the presence of a host link as the computer starts.

Identifying FPGA Devices in MAX

Complete the following steps to verify that the FPGA devices appear properly in MAX.

1. Launch MAX.
2. In the Configuration pane, expand **Devices and Interfaces** to see the list of installed devices. Installed devices appear under the name of their associated chassis.
3. Verify that the parameters of your host-side MXI adapter, such as slot and port numbers, match the ID of the FPGA to which the cable is connected.

Note Each FPGA will have a default name, such as ATCA-3671

"PXI1Slot2Port1Dev1-A," based on the configuration of your system.

Tip Find additional information, such as the model and serial number of the ATCA-3671 main board, as well as the FPGA ID of the connected FPGA, by selecting any ATCA-3671 FPGA device and opening the **Settings** window.

Related tasks:

- Why Doesn't the Device Appear in MAX?

Network Connection Configuration to the ATCA-3671

An Ethernet connection to the host computer is required to use the ATCA-3671 hardware.

The ATCA-3671 is shipped with the following fixed network configuration:

- IP address: 192.168.0.75
- Subnet mask: 255.255.255.0
- Gateway address: 192.168.0.1

This document assumes that the ATCA-3671 uses the default network configuration above, and that the host computer has been configured with an adapter able to communicate on the same network.

Programming

You can program the ATCA-3671 FPGA with LabVIEW FPGA.

Note This section does not apply to programming using BPS. Visit ni.com/info and enter the Info Code `BPSsupport` to access resources within the BPS Support Community.

Finding Example VIs

The NI-ATCA FPGA Modules examples provide a starting point for programming the ATCA-3671 with LabVIEW. Complete the following steps to browse or search installed example VIs and online example VIs.

1. Within LabVIEW, select **Help** » **Find Examples** to launch the NI Example Finder.
2. Select the **Browse** tab to locate examples by task at **Hardware Input and Output** » **ATCA**.

> **Note** If you add LLB or VI files to the `labview\examples` directory, you can select **Help** » **Find Examples** to browse them by directory structure. To browse examples by functionality or search by keywords, you can prepare example VIs to appear in the NI Example Finder. You must add example VIs to the NI Example Finder as top-level VIs.

Verifying the Device Connection (Optional)

Run a VI to confirm the device is correctly connected to the host computer.

1. Within LabVIEW, select **Help** » **Find Examples** and search `ATCA`, then open `ATCA 3671 - Getting Started DRAM.lvproj`.
2. Open the Getting Started - External Memory (Host) VI in the project.
3. Configure the `RIO Device` input for the FPGA you want to use.
4. Run the VI.
   If the device is working properly, the data written to memory is the data read on the indicators with no errors.
5. Click **STOP** to conclude the test.

Removing the ATCA-3671 Module

1. Disconnect all cables from the ATCA-3671 module.
2. Loosen the four captive screws on the front panel of the ATCA-3671 module.
3. Push up on the latch and rotate the ejector handle away from the module to unseat the backplane connectors to open the ejector handles.
4. Carefully remove the ATCA-3671 module from the chassis. Use two hands while removing the module to ensure that the module is not dropped once it is free of the chassis.

Removing the RTM

1. Disconnect all cables from the RTM.
2. Loosen the four captive screws on the back panel of the RTM.
3. Use the ejector handles to unseat the RTM from the chassis.
4. Carefully remove the RTM from the chassis. Use two hands while removing the module to ensure that the module is not dropped once it is free of the chassis.

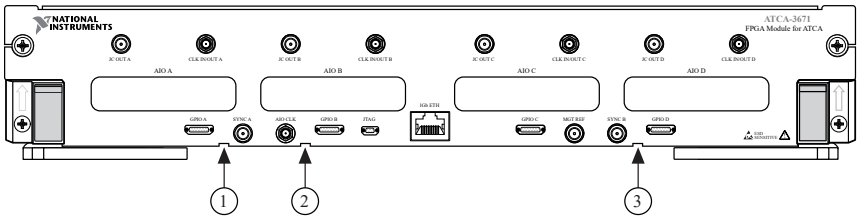
Removing the ATCA I/O Module

Complete the following steps to remove an ATCA I/O module from the ATCA-3671.

1. Disconnect all cables to the ATCA I/O module.
2. Power off the ATCA-3671.
3. Remove the ATCA-3671 module.
4. Unscrew the ATCA I/O module.
5. Remove the ATCA I/O module.

ATCA-3671 Front Panel

Figure 13. ATCA-3671 Front Panel



1. Red LED
2. Green LED
3. Blue LED

Note The signal pins of this product's input/output ports can be damaged if subjected to ESD. To prevent damage, turn off power to the product before connecting cables and employ industry-standard ESD prevention measures during installation, maintenance, and operation.

Table 2. ATCA-3671 Front Panel Connectors

Connector	Description
JC OUT	Conditioned output clock for CPRI and clock distribution applications.
CLK IN/OUT	General purpose clock to and from the FPGA.
AIO	Daughtercards for RF front-ends and high-bandwidth expansion modules.
GPIO	Low speed, parallel I/O expansion to your module.
SYNC	Low skew trigger and clock distribution to FPGAs.
AIO CLK	Direct clock distribution to AIO modules.
JTAG	JTAG/UART access for system debugging and management.
1Gb ETH	Network connection to ATCA-3671 controller.
MGT REF	Clock input for GTH reference on FPGAs.

Notice Connections that exceed any of the maximum ratings of any connector on the ATCA-3671 can damage the device and the chassis. NI is not liable for any damage resulting from such connections.

Table 3. ATCA-3671 Front Panel LEDs

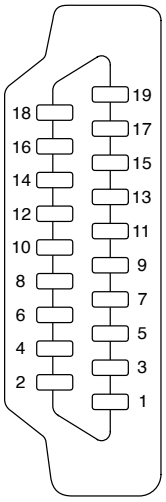
Color	State	Indication
Green	Blinking	The module is not fully powered on.
	Solid	The module is ready for use.
Red	Blinking	A fault has occurred on the

Color	State	Indication
		board.
	Solid	The power has failed.
Blue	Blinking	The module is powering on or there has been a failure.
	Solid	The module is fully powered off. It is safe to remove the ATCA-3671 from the chassis.

Note When the LED lights are all off, the module is not active.

GPIO Connector

Table 4. ATCA-3671 GPIO Connector Pin Assignments

AUX I/O Connector	Pin	Signal	Signal Description
	1	GND	Bidirectional single-ended (SE) digital I/O (DIO) data channel.
	2	FP_GPIO_CONN0	Ground reference for signals.
	3	GND	Bidirectional SE DIO data channel.
	4	GND	Bidirectional SE DIO data channel.
	5	FP_GPIO_CONN1	Ground reference for signals.
	6	GND	Ground reference for signals.
	7	GND	Bidirectional SE DIO data channel.
	8	FP_GPIO_CONN2	Ground reference for signals.
	9	FP_GPIO_CONN3	Bidirectional SE DIO

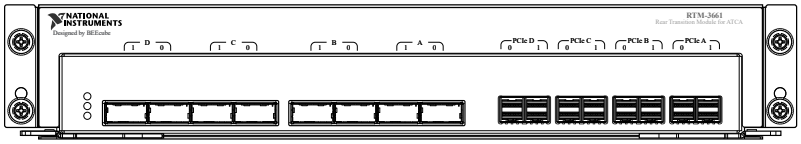
AUX I/O Connector	Pin	Signal	Signal Description
			data channel.
	10	GND	Bidirectional SE DIO data channel.
	11	FP_GPIO_CONN4	Ground reference for signals.
	12	FP_GPIO_CONN5	Bidirectional SE DIO data channel.
	13	GND	Bidirectional SE DIO data channel.
	14	FP_GPIO_CONN6	Bidirectional SE DIO data channel.
	15	FP_GPIO_CONN7	Bidirectional SE DIO data channel.
	16	FP_GPIO_CONN8	Bidirectional SE DIO data channel.
	17	FP_GPIO_CONN9	Ground reference for signals.
	18	VCC_GPIO	+5 V Power (<1.0 A).
	19	GND	Ground reference for signals.

Notice The GPIO connector accepts a standard, third-party Mini HDMI cable, but the GPIO port is not a Mini HDMI interface. Do not connect the ATCA-3671 to another device using the GPIO port as a Mini HDMI connection. NI is not liable for any damage resulting from such signal connections.

Note The maximum GPIO speed is 10 MHz.

RTM-3661 Front Panel

Figure 14. RTM-3661 Front Panel



Notice Connections that exceed any of the maximum ratings of any connector on the ATCA-3671 can damage the device and the chassis. NI is not liable for any damage resulting from such connections.

ATCA Compatibility vs. Compliance

The ATCA-3671 achieves mechanical and electrical compatibility with the ATCA (Advanced Telecommunications Computing Architecture) 3.0 specification, and it is intended for use with an ATCA 3.0-compliant Shelf. However, the ATCA-3671 is not a fully compliant Front Board implementation. For example, it does not support hardware platform management from a Shelf Manager. In addition, while it implements functions for Ethernet on the Base Interface and clocks on the Synchronization Clock Interface, it does not support Electronic Keying.

Troubleshooting

If an issue persists after you complete a troubleshooting procedure, search our KnowledgeBase for additional information our technical support engineers create as they answer common user questions and resolve unexpected issues.

Should I Update Device Firmware and FPGA Images?

ATCA-3671 devices ship with firmware and FPGA images compatible with NI-ATCA FPGA Modules driver software.

You may need to update the device for compatibility with the latest version of the software. Visit ni.com/downloads to find firmware updates, driver updates, and patches.

Why Doesn't the Device Power On?

If you cannot power on the device, complete the following steps.

- Verify that the device is connected to the power supply.
- Verify that the power supply is functional.
- Verify that the module is fully inserted into the chassis.
- Verify that the ejector handles are locked and the captive screws are tightened.

Why Doesn't the Device Appear in MAX?

If you cannot find the ATCA-3671 FPGAs in MAX, complete one or more of the following steps.

1. Ensure you have the correct version of NI-ATCA FPGA Modules installed on the host computer.
2. Check the MXI module status LEDs and MXI cable connections to the ATCA-3671 and host computer.
3. Restart the host computer to reset the MXI connection.
4. Wait at least 90 seconds after powering on the ATCA before powering on the host computer.
5. Verify that a valid bitstream has been downloaded to all FPGAs. Interrupted bitstream transfers or downloads may cause PCIe enumeration to fail.
6. Use the Remote Bitstream Download VI to force a download if you suspect a transfer or download was interrupted. A host computer restart is required after preforming a remote download.

Related tasks:

- Identifying FPGA Devices in MAX

Why do I Repeatedly Receive Error -61018?

You will receive this error if you complete the two steps to deploy a bitfile to the ATCA in the incorrect order.

Ensure that you transfer a bitstream to the ATCA's memory using Ethernet before you open a PCIe RIO session. Use the ATCA-3671 Open Reference VI to automatically complete these steps in the correct order. Inspect the block diagram of this VI to better understand its function.

This error is likely caused by having a different bitfile in the ATCA's memory than the bitfile specified in the RIO Open FPGA VI Reference. Verify the following:

- You successfully transferred the correct bitstream to the ATCA using Ethernet.
- The `bitfile path` specified in the ATCA-3671 Transfer Bitstream VI and the `bitfile path` specified in the Open FPGA Reference VI are the same.
- The specified `FPGA ID` in the ATCA-3671 Transfer Bitstream VI is correct.

Refer to ATCA-3671 FPGA Module for ATCA examples to reference correct bitfile deployment.

Why do I Repeatedly Receive Error -63150?

This error may be caused by incorrect MXI cabling.

Ensure that you cabled MXI connections to ports in ascending order.
Related tasks:

- Installing MXI Cables

Why do I Repeatedly Receive Error -56?

This error is caused by a TCP timeout during attempts to communicate with the ATCA-3671 over Ethernet.

Note The default IP for ATCA hardware is a private network address (192.168.0.75) that can be reconfigured through SSH per the IT policies of the end user. The ATCA should be cabled directly to a dedicated NIC on a host PC or PXI controller for initial setup.

To avoid this error, complete the following steps:

- Verify that you have specified the correct IP address for the ATCA SOM.
- Verify that the ATCA-3671 and the host PC are properly connected with an Ethernet cable.
- Verify the Ethernet port configuration of the host PC is correct. A unique private network address on the same subnet is required for initial setup.
- Attempt to ping the ATCA default IP from a command prompt on your host machine.
- If you are unable to connect to the ATCA-3671, contact your IT administrator for general Ethernet troubleshooting assistance or NI support for ATCA-specific assistance.