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# PCI-5153

# Specifications

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2025-03-14



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# PCI-5153 Specifications

## Definitions

**Warranted** specifications describe the performance of a model under stated operating conditions and are covered by the model warranty. Warranted specifications account for measurement uncertainties, temperature drift, and aging. Warranted specifications are ensured by design or verified during production and calibration.

**Characteristics** describe values that are relevant to the use of the model under stated operating conditions but are not covered by the model warranty.

- **Typical** specifications describe the performance met by a majority of models.
- **Nominal** specifications describe an attribute that is based on design, conformance testing, or supplemental testing.
- **Measured** specifications describe the measured performance of a representative model.

Specifications are **Typical** unless otherwise noted.

## Conditions

Specifications are valid under the following conditions unless otherwise noted.

- All filter settings.
- All impedance selections.
- Sample clock set to 1 GS/s.
- Real-Time Interleaved Sampling (TIS) mode provides a 2 GS/s real-time sample rate for a single channel.
- The module is warmed up for 15 minutes at ambient temperature.
- Self-calibration is completed after warm-up period.
- Calibration cycle is maintained.
- The PXI/PCI chassis fan speed is set to HIGH, the foam fan filters are removed if

present, and the empty slots contain slot blockers and filler panels. For more information about cooling, refer to the ***Maintain Forced-Air Cooling Note to Users***.

## Vertical

### Analog Input (Channel 0 and Channel 1)

Number of channels	Two (simultaneously sampled)
Connectors	BNC

### Impedance and Coupling

Input impedance	$50\ \Omega \pm 2\ \Omega$
Voltage Standing Wave Ratio (VSWR)	1.2 DC to 500 MHz
Input coupling	AC, DC, GND

### Voltage Levels

Full scale (FS) input ranges ( $V_{pk-pk}$ )	0.1 V 0.2 V 0.5 V 1 V
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	2 V 5 V
Maximum input overload	7 V RMS with $ Peaks  \leq 10$ V

## Accuracy

Resolution	8 bits
<b>DC accuracy</b> <sup>[1]</sup>	
0.1 V to 1 V input range	$\pm(1.0\% \text{ of input} + 1.3\% \text{ of FS}), \text{ typical}$ $\pm(2.2\% \text{ of input} + 1.8\% \text{ of FS}), \text{ warranted}$
2 V to 5 V input range	$\pm(1.4\% \text{ of input} + 1.3\% \text{ of FS}), \text{ typical}$ $\pm(2.9\% \text{ of input} + 1.8\% \text{ of FS}), \text{ warranted}$
DC drift <sup>[2]</sup>	$\pm(0.14\% \text{ of input} + 0.05\% \text{ of FS}) \text{ per } ^\circ\text{C}, \text{ nominal}$
<b>Crosstalk</b>	
CH 0 to/from CH 1 <sup>[3]</sup>	$<-80 \text{ dB at } 10 \text{ MHz}$ $<-60 \text{ dB at } 100 \text{ MHz}$
External trigger to CH 0 or CH 1 <sup>[4]</sup>	$<-80 \text{ dB at } 10 \text{ MHz}$ $<-70 \text{ dB at } 100 \text{ MHz}$

# Bandwidth and Transient Response

Bandwidth (-3 dB) <sup>[5],[6]</sup>	500 MHz minimum, warranted
Rise/fall time <sup>[6]</sup>	600 ps
Bandwidth limit filter	20 MHz noise filter
AC coupling cutoff (-3 dB) <sup>[7]</sup>	114 kHz

Figure 1. PCI-5153 Frequency Response, 5 V<sub>pk-pk</sub> through 0.1 V<sub>pk-pk</sub> Input Ranges, Measured

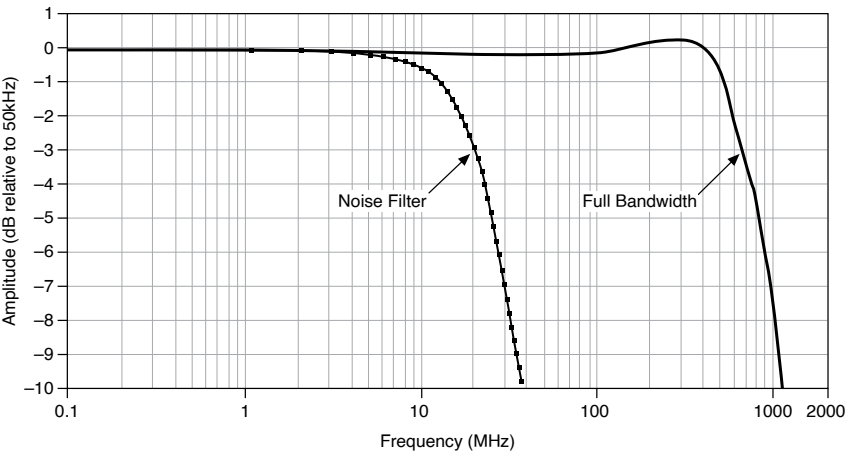
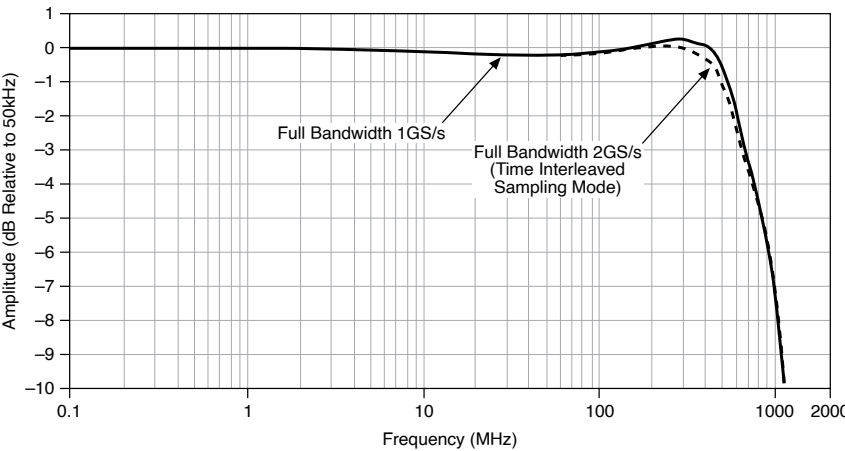
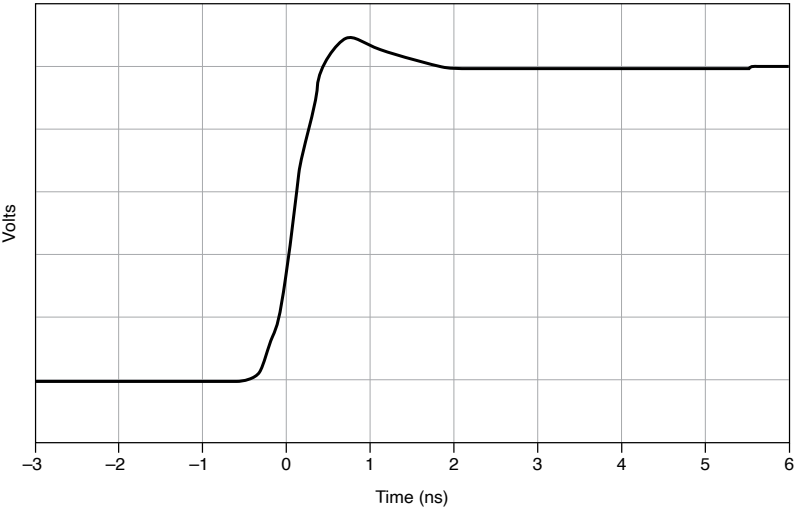


Figure 2. PCI-5153 Frequency Response, 5 V<sub>pk-pk</sub> through 0.1 V<sub>pk-pk</sub> Input Ranges, Measured



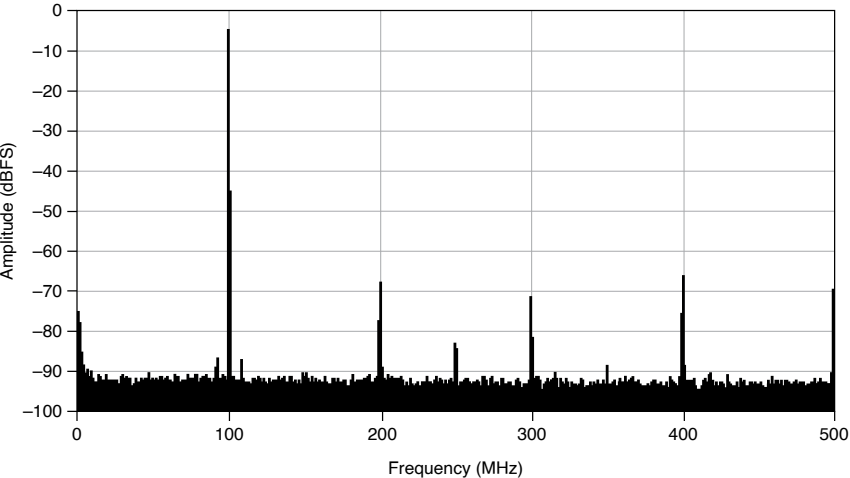
**Figure 3.** PCI-5153 Step Response, 5  $V_{pk-pk}$  through 0.1  $V_{pk-pk}$  Input Ranges, Measured



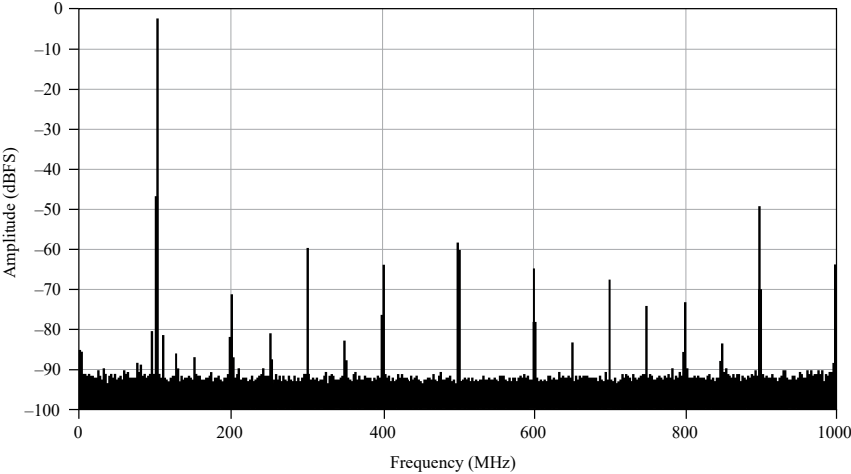
Spectral Characteristics

ENOB <sup>[8]</sup>	
Noise filter on	7.3
Noise filter off	6.7
Signal to noise distortion (SINAD) <sup>[8]</sup>	
Noise filter on	45 dB
Noise filter off	41 dB

**Figure 4.** PCI-5153 Dynamic Performance, 2 V<sub>pk-pk</sub> Input Range, 100 MHz Input Signal, Measured



**Figure 5.** PCI-5153 TIS Dynamic Performance, 2 V<sub>pk-pk</sub> Input Range, 100 MHz Input Signal, Measured



Noise

**Table 1.** RMS Noise<sup>[9]</sup>

Range (V <sub>pk-pk</sub> )	Noise Filter On	Noise Filter Off	Noise Filter On TIS Mode	Noise Filter Off TIS Mode
0.1	0.37% FS	0.44% FS	0.41% FS	0.71% FS
0.2 to 5	0.32% FS	0.32% FS	0.41% FS	0.41% FS

Channel-to-channel skew	<140 ps
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# Horizontal

## Sample Clock

Sources	
Internal	Onboard clock (internal VCISO) <sup>[10]</sup>
External	PFI 0 (front panel SMB connector)
Timebase frequency	1 GHz <sup>[11]</sup>

## Onboard Clock (Internal VCISO)

Sample rate range	
Real-time sampling (single shot) <sup>[12]</sup>	15.26 kS/s to 1 GS/s
TIS <sup>[13]</sup> mode (single shot)	2 GS/s (single channel only)
Random interleaved sampling (RIS) mode <sup>[14]</sup>	2 GS/s to 20 GS/s in increments of 1 GS/s (repetitive waveforms only)
Timebase accuracy	
Not phase-locked to Reference clock	$\pm 30$ ppm within $\pm 3$ °C of external calibration temperature, plus an additional $\pm 7$ ppm per °C outside of $\pm 3$ °C of external calibration temperature, warranted
Phase-locked to	Equal to the Reference clock accuracy <sup>[15]</sup>

Reference clock	
Sample clock delay range	$\pm 1$ Sample clock period
Sample clock delay/adjustment resolution	$\leq 5$ ps

## External Sample Clock

Sources	PFI 0 (front panel SMB connector)
Frequency range <sup>[16]</sup>	350 MHz to 1 GHz
Duty cycle tolerance	45% to 55%

## Phase-Locked Loop (PLL) Reference Clock

Sources	RTSI 7 PFI 0 (front panel SMB connector)
Frequency range <sup>[17]</sup>	1 MHz to 20 MHz, in 1 MHz increments Default: 10 MHz
Duty cycle tolerance	45% to 55%

Exported Reference clock destinations	RTSI <0..7>  PFI 1 (front panel SMB connector)
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## Sample Clock and Reference Clock Input (PFI 0, Front Panel Connector)

Input voltage range	Sine wave: $0.65 V_{pk-pk}$ to $2.8 V_{pk-pk}$  (0 dBm to 13 dBm)
Maximum input overload	7 V RMS with $ Peaks  \leq 10 V$
Impedance	50 $\Omega$
Coupling	AC

## Reference Clock Output (PFI 1, Front Panel Connector)

Output impedance	50 $\Omega$
Logic type	3.3 V CMOS, except when exporting 5 V
Maximum drive current	$\pm 24$ mA

# Trigger

Trigger types <sup>[18]</sup>	Edge
	Window
	Hysteresis
	Video
	Digital
	Immediate
	Software
Trigger sources	CH 0
	CH 1
	TRIG
	PFI <0..1>
	RTSI <0..6>
Software	
Time resolution	
Onboard clock, time-to-digital conversion circuit (TDC) on	5 ps
Onboard clock, TDC off	1 ns
External clock, TDC off	External clock period
Minimum rearm time <sup>[19]</sup>	

TDC on	8 $\mu$ s
TDC off	1 $\mu$ s
Holdoff	From rearm time up to $[(2^{32} - 1) \times \textit{Sample Clock Period}]$
Trigger delay	From 0 up to $[(2^{35} - 1) - \textit{Posttrigger Samples}] \times (1 / \textit{Sample Rate})$ , in seconds

## Analog Trigger

Trigger types	Edge Window Hysteresis
Sources	CH 0 (front panel BNC connector) CH 1 (front panel BNC connector) TRIG (front panel BNC connector)
<b>Trigger level range</b>	
CH 0, CH 1	100% FS
TRIG (External trigger)	$\pm 5$ V
Voltage resolution	8 bits (1 in 256)

Edge trigger sensitivity: CH 0, CH 1, TRIG (External trigger)		
DC to 300 MHz		10% FS, warranted
>300 MHz to 500 MHz		10% FS, typical
Trigger level accuracy <sup>[20]</sup>		
CH 0, CH 1	±5% FS up to 10 MHz, warranted	
TRIG (External trigger)	±1 V (±10% FS) up to 10 MHz, warranted	
Trigger jitter <sup>[20]</sup>	≤14 ps RMS, typical	
	20 ps RMS maximum	
Trigger filters		
Low frequency (LF) reject		50 kHz
High frequency (HF) reject		50 kHz

## Digital Trigger

Trigger type	Digital
Sources	RTSI <0..6> PFI <0..1> (front panel SMB connector)

## External Trigger Input (Front Panel Connector)

Connector	BNC
Impedance	2.25 k $\Omega$
Coupling	DC
Input voltage range	$\pm 5$ V
Maximum input overload	Peaks  $\leq 10$ V

## PFI 0 and PFI 1 (Programmable Function Interface, Front Panel Connectors)

Connector	SMB jack
Direction	Bidirectional

## As an Input (Trigger)

Destination	Start trigger (acquisition arm) Reference (stop) trigger Arm reference trigger Advance trigger
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Input impedance	150 k $\Omega$ , nominal
V <sub>IH</sub>	2.0 V
V <sub>IL</sub>	0.8 V
Maximum input overload	-0.5 V to 5.5 V
Maximum frequency	25 MHz

## As an Output (Event)

Sources	<p>Ready for Start</p> <p>Start trigger (acquisition arm)</p> <p>Ready for Reference</p> <p>Reference (stop) trigger</p> <p>End of Record</p> <p>Ready for Advance</p> <p>Advance trigger</p> <p>Done (end of acquisition)</p> <p>Probe Compensation<sup>[21]</sup></p>
Output impedance	50 $\Omega$



Logic type	3.3 V CMOS
Maximum drive current	$\pm 24$ mA
Maximum frequency	25 MHz

## Waveform Specifications

**Table 2.** Onboard Memory Size

Real-Time and RIS Modes	Real-Time TIS Mode
8 MB standard (8 MS per channel)	8 MB standard (8 MS)
64 MB option (64 MS per channel)	64 MB option (64 MS)
256 MB option (256 MS per channel)	256 MB option (256 MS)

Minimum record length	1 sample
Number of pretrigger samples <sup>[22]</sup>	Zero up to full record length
Number of posttrigger samples <sup>[22]</sup>	Zero up to full record length
Maximum number of records in onboard memory <sup>[23]</sup>	
8 MB per channel	32,768
64 MB per channel	100,000

256 MB per channel	100,000
Allocated onboard memory per record	$+(\textit{Record Length} \times 1 \text{ byte/sample}) + 400 \text{ bytes} + \text{rounded up to next multiple of 128 bytes}$

## Calibration

### External Calibration

External calibration calibrates the onboard references used in self-calibration and the external trigger levels. All calibration constants are stored in nonvolatile memory.

### Self-Calibration

Self-calibration is done on software command. The calibration corrects for gain, offset, triggering, and timing errors for all input ranges, excluding External Trigger input channel.

### Calibration Specifications

Interval for external calibration	2 years
Warm-up time <sup>[24]</sup>	15 minutes

## Software

### Driver Software

Driver support for this device was first available in NI-SCOPE3.5.

NI-SCOPE is an IVI-compliant driver that allows you to configure, control, and calibrate

the PCI-5153. NI-SCOPE provides application programming interfaces for many development environments.

## Application Software

NI-SCOPE provides programming interfaces, documentation, and examples for the following application development environments:

- LabVIEW
- LabWindows™/CVI™
- Measurement Studio
- Microsoft Visual C/C++
- .NET (C# and VB.NET)

## Interactive Soft Front Panel and Configuration

When you install NI-SCOPE on a 64-bit system, you can monitor, control, and record measurements from the PCI-5153 using InstrumentStudio.

InstrumentStudio is a software-based front panel application that allows you to perform interactive measurements on several different device types in a single program.



**Note** InstrumentStudio is supported only on 64-bit systems. If you are using a 32-bit system, use the NI-SCOPE-specific soft front panel instead of InstrumentStudio.

Interactive control of the PCI-5153 was first available via InstrumentStudio in NI-SCOPE18.1 and via the NI-SCOPE SFP in NI-SCOPE3.5. InstrumentStudio and the NI-SCOPE SFP are included on the NI-SCOPE media.

NI Measurement & Automation Explorer (MAX) also provides interactive configuration and test tools for the PCI-5153. MAX is included on the driver media.

## TClk Specifications

You can use the NI TClk synchronization method and the NI-TClk driver to align the

Sample clocks on any number of supported devices, in one or more chassis. For more information about TClk synchronization, refer to the ***NI-TClk Synchronization Help***, which is located within the ***NI High-Speed Digitizers Help***. For other configurations, including multichassis systems, contact NI Technical Support at [ni.com/support](https://ni.com/support).

#### Intermodule SMC Synchronization Using NI-TClk for Identical Modules

Synchronization specifications are valid under the following conditions:

- All modules are installed in one NI PXI-1042 chassis
- The NI-TClk driver is used to align the Sample clocks of each module
- All parameters are set to identical values for each module
- Modules are synchronized without using an external Sample clock
- Sample clock set to 1 GS/s and all filters are disabled



**Note** Although you can use NI-TClk to synchronize non-identical SMC-based modules, these specifications apply only to synchronizing identical modules.

Skew	500 ps
Skew after manual adjustment	≤5 ps
Sample clock delay/adjustment resolution	≤5 ps

## Power

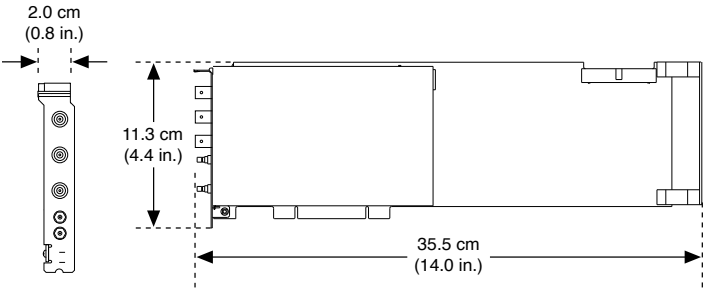
Current draw	
+3.3 V DC	2.5 A

+5 V DC	2.4 A
+12 V DC	250 mA
Total power	23.25 W

Physical

Dimensions	35.5 cm × 2.0 cm × 11.3 cm (14.0 in × 0.8 in × 4.4 in)
Weight	439 g (15.5 oz)

Figure 6. PCI-5153 Dimensions



Environment

Maximum altitude	2,000 m (at 25 °C ambient temperature)
Pollution Degree	2

Indoor use only.

## Operating Environment

Ambient temperature range	0 °C to 45 °C (Tested in accordance with IEC 60068-2-1 and IEC 60068-2-2.)
Relative humidity range	10% to 90%, noncondensing (Tested in accordance with IEC 60068-2-56.)

## Storage Environment

Ambient temperature range	-40 °C to 71 °C (Tested in accordance with IEC 60068-2-1 and IEC 60068-2-2.)
Relative humidity range	5% to 95%, noncondensing (Tested in accordance with IEC 60068-2-56.)

## Compliance and Certifications

### Safety Compliance Standards

This product is designed to meet the requirements of the following electrical equipment safety standards for measurement, control, and laboratory use:

- IEC 61010-1, EN 61010-1
- UL 61010-1, CSA C22.2 No. 61010-1



**Note** For safety certifications, refer to the product label or the [Product Certifications and Declarations](#) section.

## Electromagnetic Compatibility

This product meets the requirements of the following EMC standards for electrical equipment for measurement, control, and laboratory use:

- EN 61326-1 (IEC 61326-1): Class A emissions; Basic immunity
- EN 55011 (CISPR 11): Group 1, Class A emissions
- EN 55022 (CISPR 22): Class A emissions
- EN 55024 (CISPR 24): Immunity
- AS/NZS CISPR 11: Group 1, Class A emissions
- AS/NZS CISPR 22: Class A emissions
- FCC 47 CFR Part 15B: Class A emissions
- ICES-001: Class A emissions



**Note** In the United States (per FCC 47 CFR), Class A equipment is intended for use in commercial, light-industrial, and heavy-industrial locations. In Europe, Canada, Australia, and New Zealand (per CISPR 11), Class A equipment is intended for use only in heavy-industrial locations.



**Note** Group 1 equipment (per CISPR 11) is any industrial, scientific, or medical equipment that does not intentionally generate radio frequency energy for the treatment of material or inspection/analysis purposes.



**Note** For EMC declarations, certifications, and additional information, refer to the [Product Certifications and Declarations](#) section.

## CE Compliance

This product meets the essential requirements of applicable European Directives, as follows:

- 2014/35/EU; Low-Voltage Directive (safety)
- 2014/30/EU; Electromagnetic Compatibility Directive (EMC)
- 2011/65/EU; Restriction of Hazardous Substances (RoHS)

## Product Certifications and Declarations


Refer to the product Declaration of Conformity (DoC) for additional regulatory compliance information. To obtain product certifications and the DoC for NI products, visit [ni.com/product-certifications](https://ni.com/product-certifications), search by model number, and click the appropriate link.

## Environmental Management


NI is committed to designing and manufacturing products in an environmentally responsible manner. NI recognizes that eliminating certain hazardous substances from our products is beneficial to the environment and to NI customers.

For additional environmental information, refer to the ***Engineering a Healthy Planet*** web page at [ni.com/environment](https://ni.com/environment). This page contains the environmental regulations and directives with which NI complies, as well as other environmental information not included in this document.

### EU and UK Customers

-  **Waste Electrical and Electronic Equipment (WEEE)**—At the end of the product life cycle, all NI products must be disposed of according to local laws and regulations. For more information about how to recycle NI products in your region, visit [ni.com/environment/weee](https://ni.com/environment/weee).

### 电子信息产品污染控制管理办法（中国RoHS）

-  **中国RoHS**—NI符合中国电子信息产品中限制使用某些有害物质指令 (RoHS)。关于NI中国RoHS合规性信息，请登录 [ni.com/environment/rohs\\_china](https://ni.com/environment/rohs_china)。(For information about China RoHS compliance, go to [ni.com/environment/rohs\\_china](https://ni.com/environment/rohs_china).)