
PCI-5922

Specifications

2025-03-10



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PCI-5922 Specifications

Definitions

Warranted specifications describe the performance of a model under stated operating conditions and are covered by the model warranty. Warranted specifications account for measurement uncertainties, temperature drift, and aging. Warranted specifications are ensured by design or verified during production and calibration.

Characteristics describe values that are relevant to the use of the model under stated operating conditions but are not covered by the model warranty.

- **Typical** specifications describe the performance met by a majority of models.
- **Nominal** specifications describe an attribute that is based on design, conformance testing, or supplemental testing.
- **Measured** specifications describe the measured performance of a representative model.

Specifications are **Typical** unless otherwise noted.

Conditions

Specifications are valid under the following conditions unless otherwise noted.

- Full operating temperature range
- All impedance selections
- All sample rates
- Source impedance $\leq 50 \Omega$

Specifications are valid under the following conditions unless otherwise noted:

- Ambient temperatures of 15 °C to 35 °C

PCI-5922 Pinout

Use the pinout to connect to the AUX I/O connector on the PCI-5922.

Figure 1. PCI-5922 9-Pin DIN Connector

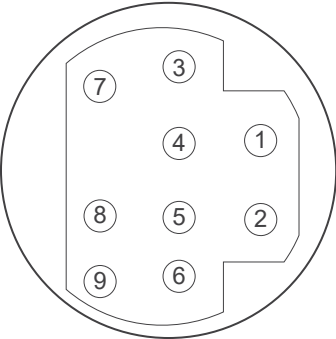



Table 1. Pin Assignments and Connector Descriptions

Pin Number	Connector Description
1	5 V (Fused)
2	GND
3	Reserved
4	Reserved
5	Reserved
6	PFI 1
7	Reserved
8	Reserved
9	PFI 0



Note Be sure to use an NI adapter cable or a cable that has the same pinout shown in the previous figure.

Vertical

Analog Input

Number of channels	Software-selectable: two simultaneously sampling, single-ended or unbalanced differential channels or one differential channel
Connector	BNC

Impedance and Coupling

Input impedance	Software-selectable: $50\ \Omega \pm 2.0\%$ or $1\ \text{M}\Omega \pm 2.0\%$ in parallel with a nominal capacitance of 60 pF
Input coupling	AC, DC, GND

Voltage Levels

Full-scale (FS) input range	$\pm 1\ \text{V}$ ($2\ V_{\text{pk-pk}}$) $\pm 5\ \text{V}$ ($10\ V_{\text{pk-pk}}$)
Maximum input overload	
$50\ \Omega$	7 V RMS with $ \text{Peaks} \leq 10\ \text{V}$
$1\ \text{M}\Omega$	$ \text{Peaks} \leq 42\ \text{V}$

Accuracy

Table 2. PCI-5922 Resolution

Sample Rate	Resolution
50 kS/s	24 bits
500 kS/s	24 bits
1 MS/s	22 bits
5 MS/s	20 bits
10 MS/s	18 bits
15 MS/s	16 bits

DC accuracy ¹	
2 V _{pk-pk} range	±(0.05% of input + 50 µV), warranted
10 V _{pk-pk} range	±(0.05% of input + 100 µV), warranted
DC drift ²	
2 V _{pk-pk} range	±(0.002% of input + 5 µV per °C), nominal
10 V _{pk-pk} range	±(0.002% of input + 10 µV per °C), nominal

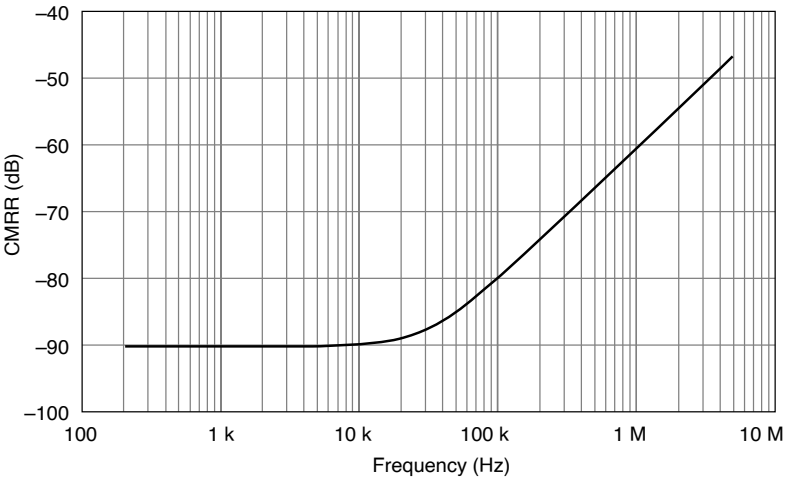
AC amplitude accuracy	0.06% at 1 kHz ³
Crosstalk ⁴	

1. 1 MΩ input impedance; within ±5 °C of self-calibration temperature.
2. 1 MΩ input impedance.
3. 1 MΩ input impedance; within ±5 °C of self-calibration temperature.
4. CH 0 to/from CH 1, External Trigger to CH 0 or CH 1.

At 100 kHz	≤-110 dB
At 1 MHz	≤-100 dB
At 6 MHz	≤-80 dB

Common-mode rejection ratio (CMRR)	50 dB up to 1 kHz ⁵
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Figure 2. PCI-5922 CMRR with Differential Terminal Configuration, Measured



Bandwidth and Transient Response

Alias-free bandwidth	$0.4 \times \textit{Sample Rate}$
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5. Unbalanced differential input terminal configuration.

Table 3. Alias Protection⁶

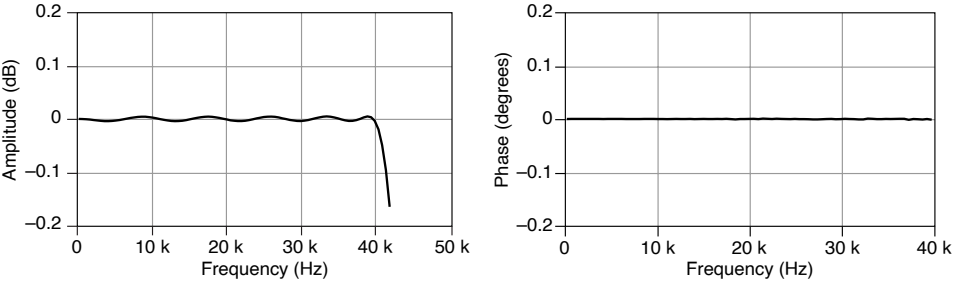
Sample Rate	Attenuation
<5 MS/s	100 dB
5 MS/s	96 dB
(5 MS/s, 7.5 MS/s)	90 dB
[7.5 MS/s, 15 MS/s]	80 dB

AC coupling cutoff (-3 dB)	90 Hz
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Table 4. Passband Flatness⁷

Sample Rate	50 Ω and 1 MΩ
1 MS/s	0.03 dB
5 MS/s	0.06 dB
10 MS/s	0.15 dB
15 MS/s	0.3 dB

Figure 3. 100 kS/s Frequency Response, Measured



6. Input frequencies $\geq 0.6 \times$ **Sample Rate**.
7. Referenced to DC; input frequencies up to $0.4 \times$ **Sample Rate**.

Figure 4. 1 MS/s Frequency Response, Measured

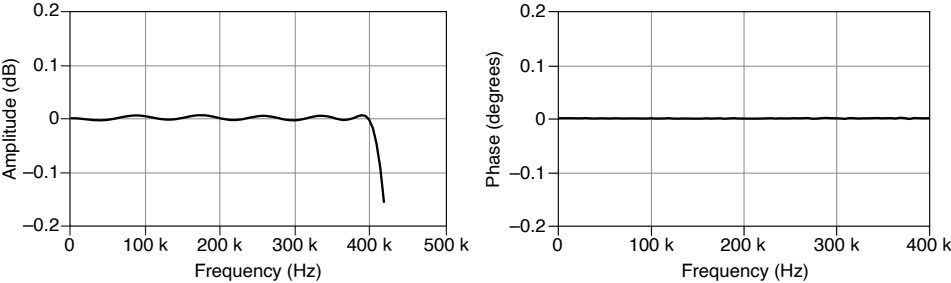
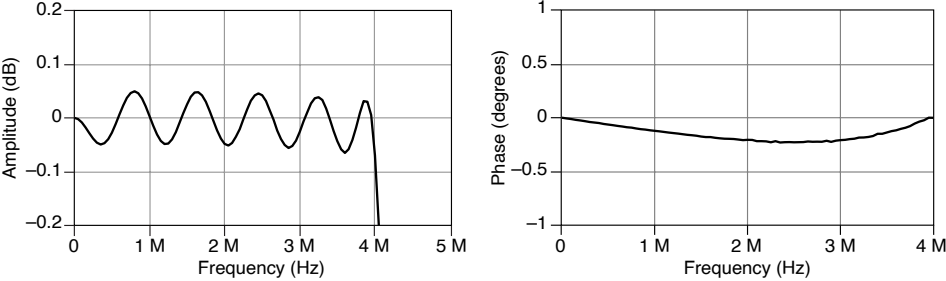


Figure 5. 10 MS/s Frequency Response, Measured



Spectral Characteristics

Table 5. Spurious-Free Dynamic Range (SFDR)⁸

Input Frequency	Range	
	10 V _{pk-pk}	2 V _{pk-pk}
10 kHz	114 dBc	109 dBc
100 kHz	110 dBc	103 dBc
1 MHz	96 dBc	92 dBc

Figure 6. PCI-5922 Dynamic Performance with 10 kHz Input Signal, Measured, 1 MΩ, 10 V_{pk-pk} Range,

8. -1 dBFS input signal; **Sample Rate** is 10 × input frequency; within ±2 °C of self-calibration temperature.

500 kS/s, Unbalanced Differential, 10,000-Point FFT with 10 Averages

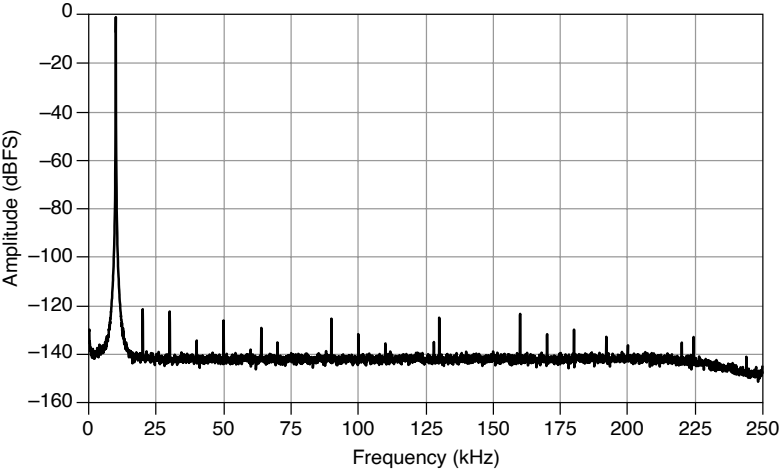


Figure 7. PCI-5922 Dynamic Performance with 10 kHz Input Signal, Measured, 1 M Ω , 2 V_{pk-pk} Range, 100 kS/s, Unbalanced Differential, 10,000-Point FFT with 10 Averages

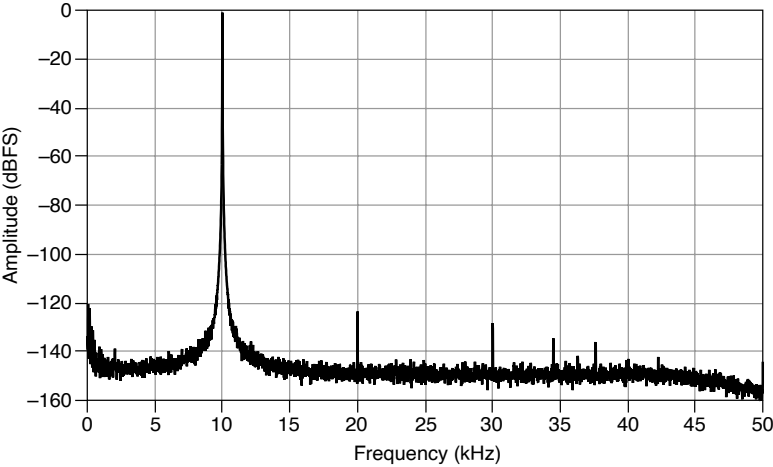


Table 6. Total Harmonic Distortion (THD)⁹

Input Frequency	Range	
	10 V _{pk-pk}	2 V _{pk-pk}
10 kHz	-112 dBc	-107 dBc
100 kHz	-108 dBc	-101 dBc
1 MHz	-94 dBc	-90 dBc

9. -1 dBFS input signal; includes the second through the fifth harmonics; within ± 2 °C of self-calibration temperature .

Table 7. Signal-to-Noise and Distortion (SINAD)¹⁰

Sample Rate	Range	
	10 V _{pk-pk}	2 V _{pk-pk}
1 MS/s	105 dB	99 dB
10 MS/s	89 dB	87 dB

Table 8. Signal-to-Noise Ratio (SNR) without Harmonics¹¹

Sample Rate	Range	
	10 V _{pk-pk}	2 V _{pk-pk}
1 MS/s	108 dB	104 dB
10 MS/s	91 dB	90 dB

Table 9. RMS Noise, Warranted¹²

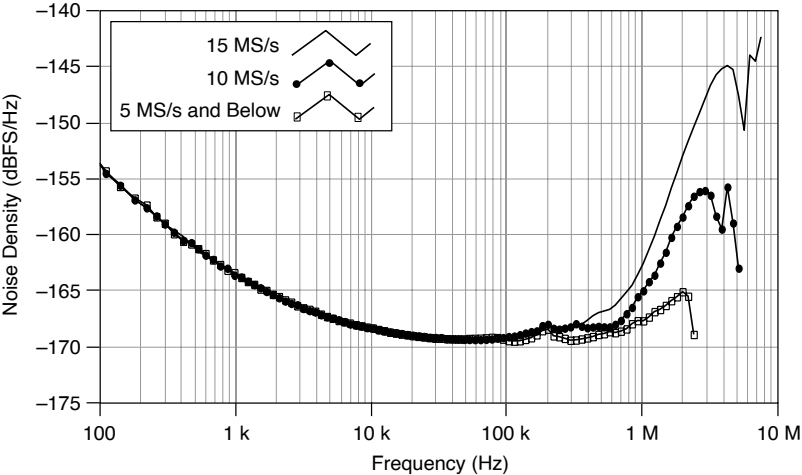
Sample Rate	Range			
	10 V _{pk-pk}		2 V _{pk-pk}	
	dBFS	μV _{rms}	dBFS	μV _{rms}
50 kS/s	-120	3.4	-110	2.2
100 kS/s	-118	4.3	-110	2.2
1 MS/s	-108	13	-104	4.2
5 MS/s	-101	31	-98	8.7
10 MS/s	-91	92	-91	20
15 MS/s	-79	401	-79	80

10. -1 dBFS input signal; input frequency is $0.1 \times \text{Sample rate}$; within $\pm 2^\circ\text{C}$ of self-calibration temperature; calculated from THD and RMS noise.

11. -1 dBFS input signal; input frequency is $0.1 \times \text{Sample rate}$; within $\pm 2^\circ\text{C}$ of self-calibration temperature; calculated from SINAD and THD.

12. 100 Hz to $0.4 \times \text{Sample rate}$; DC coupling; input $50\ \Omega$ terminated.

Figure 8. PCI-5922 Noise Density, Measured¹³



Skew, Input Bias Current

Channel-to-channel skew ¹⁴	≤500 ps
Input bias current ¹⁵	≤500 nA, warranted

Settling Time

Table 10. Settling Time¹⁶

Filter Type ¹⁷	1%	0.01%
48 Tap Standard	800 ns	2.5 μs
48 Tap Hanning	700 ns	1.5 μs
16 Tap Hanning	300 ns	1.4 μs
8 Tap Hanning	200 ns	1.3 μs

13. Plotted Without Spurs.

14. 1 MHz input, 5 MS/s sample rate.

15. Within ±5 °C of self-calibration temperature.

16. For a 3 V step from 0 V DC, excluding noise; time referenced to 1.5 V (50%) trigger; applies to 15 MS/s sample rate only.

17. To set or change the filter type, use the Flex FIR Antialias Filter Type property or the NISCOPE_ATTR_FLEX_FIR_ANTIALIAS_FILTER_TYPE attribute.

Figure 9. PCI-5922 Step Response Using Different Filter Types, Measured¹⁸

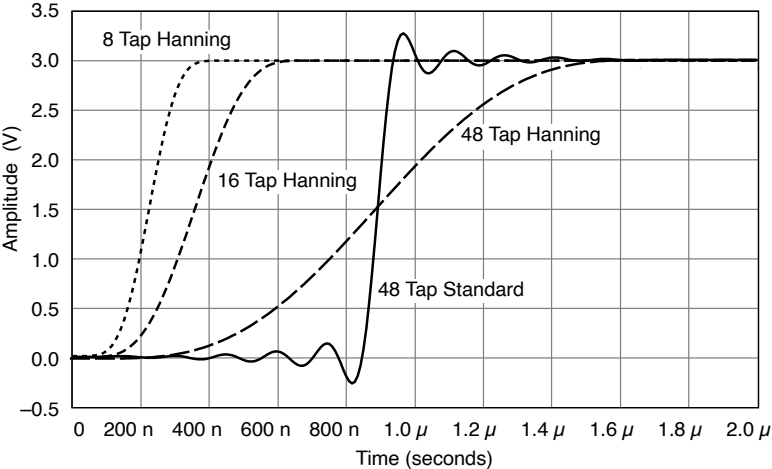
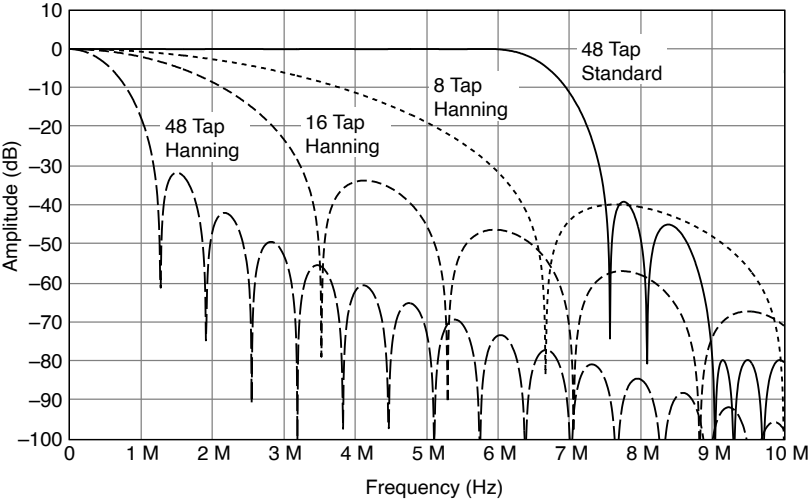


Figure 10. PCI-5922 Frequency Response Using Different Filter Types, Measured



Horizontal

Sample Clock

Sources	Internal onboard clock (internal VCXO) ¹⁹
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Onboard Clock (Internal VCXO)

Sample rate range, real-time sampling (single shot) ²⁰	50 kS/s to 15 MS/s
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18. Time (t= 0) represents the actual time the edge arrived at the BNC connector on the NI 5922.

19. Internal Sample clock is locked to the Reference clock or derived from the onboard VCXO.

Phase noise density (5 MHz input signal)	
At 10 kHz	<-133 dBc/Hz
At 100 kHz	<-145 dBc/Hz

Sample clock jitter ²¹	≤3 ps RMS (100 Hz to 1 MHz)
Timebase frequency	120 MHz
Timebase accuracy	
Not phase-locked to Reference clock	±50 ppm, warranted
Phase-locked to Reference clock	Equal to the Reference clock accuracy

Sample clock delay range	±1 Sample clock period
Sample clock delay resolution	400 ps

Phase-Locked Loop (PLL) Reference Clock

Reference clock sources	RTSI 7
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20. Available rates are (60 MS/s) /n where n is an integer value from 4 to 1200. The Sample clock period is n/(60MS/s).

21. Includes the effects of the converter aperture uncertainty and the clock circuitry jitter; excludes trigger jitter.

	CLK IN (front panel SMB connector)
Frequency range	1 MHz to 20 MHz in 1 MHz increments ²² ; must be accurate to ± 50 ppm
Duty cycle tolerance	45% to 55%
Exported Reference clock destinations	CLK OUT (front panel SMB connector) PFI <0..1> (front panel 9-pin mini-circular DIN connector) RTSI <0..7>

CLK IN (Reference Clock Input, Front Panel Connector)

Input voltage range	Square wave: $0.2 V_{pk-pk}$ to $1 V_{pk-pk}$
Maximum input overload	7 V RMS with $ Peaks \leq 10$ V
Impedance	50Ω
Coupling	AC

CLK OUT (Reference Clock Output, Front Panel Connector)

Output impedance	50Ω
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22. The default value is 10 MHz.

Logic type	5 V CMOS
Maximum drive current	±50 mA

Trigger

Reference (Stop) Trigger

Trigger types	<ul style="list-style-type: none"> Edge Window Hysteresis Digital Immediate Software
Trigger sources	<ul style="list-style-type: none"> CH 0 CH 1 TRIG PXI_Trig <0..6> PFI <0..1> PXI Star Trigger RTSI <0..6> Software

Time resolution	Sample clock period
Rearm time	$144 \times \textit{Sample clock period}^{23}$
Holdoff	Up to $(2^{32} - 1) \times \textit{Sample clock period}$

Related information:

- [Refer to the NI High-Speed Digitizers Help for more information about the sources available for each trigger type.](#)

Analog Trigger

Trigger types	Edge Window Hysteresis
Sources ²⁴	CH 0 (front panel BNC connector) CH 1 (front panel BNC connector) TRIG (front panel BNC connector)
Trigger level range	100% FS
Edge trigger sensitivity	
CH 0, CH 1	2% FS

23. Holdoff set to 0.

24. TRIG is an analog edge trigger only.

TRIG (external trigger)	0.3 V _{pk-pk} up to 1 MHz
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Jitter	Sample clock period
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Digital Trigger

Trigger type	Digital
Sources	RTSI <0..6> PFI <0..1> (front panel 9-pin DIN connector)

External Trigger

Source	TRIG (front panel BNC connector)
Impedance	100 k Ω in parallel with 52 pF, nominal
Input voltage range	± 2.5 V
Coupling	DC
Level accuracy	± 0.3 V up to 100 kHz
Maximum input overload	Peaks ≤ 42 V

PFI 0 and PFI 1 (Programmable Function Interface, AUX Front Panel Connectors)

Connector	9-pin mini-circular DIN
Direction	Bidirectional

As an Input (Trigger)

Destinations	Start trigger (acquisition arm) Reference (stop) trigger Arm Reference trigger Advance trigger
Input impedance	150 k Ω , nominal
V_{IH}	2.0 V
V_{IL}	0.8 V
Maximum input overload	-0.5 V, 5.5 V
Maximum frequency	25 MHz

As an Output (Event)

Sources	Start trigger (acquisition arm) Reference (stop) trigger End of Record Done (end of acquisition)
Output impedance	50 Ω
Logic type	3.3 V CMOS
Maximum drive current	± 24 mA
Maximum frequency	20 MHz

Waveform Specifications

Onboard memory size	
8 MB/channel	2 MS/channel
32 MB/channel	8 MS/channel
256 MB/channel	64 MS/channel

Minimum record length	1 Sample
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Number of pretrigger samples	0 up to full Record Length for both single-record mode and multiple-record mode
Number of posttrigger samples	0 up to full Record Length for both single-record mode and multiple-record mode
Maximum number of records in onboard memory²⁵	
8 MB/channel	13,107
32 MB/channel	52,428
256 MB/channel	100,000

Allocated onboard memory per record	$(\text{Record Length} \times 4 \text{ bytes/S}) + 400 \text{ bytes}$, rounded up to next multiple of 128 bytes or 640 bytes, whichever is greater
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Calibration

Self-calibration	Self-calibration is done on software command. The calibration corrects for gain and offset for all input ranges, input bias current, and nonlinearities in the ADCs.
External calibration (factory calibration)	The external calibration calibrates the VCXO and the voltage reference. Appropriate constants are stored in nonvolatile memory.

25. It is possible to exceed these numbers if you fetch records while acquiring data. For more information, refer to the ***NI High-Speed Digitizers Help***.

Interval for external calibration	2 years
Warm-up time	15 minutes

Software

Driver Software

Driver support for this device was first available in NI-SCOPE3.0.

NI-SCOPE is an IVI-compliant driver that allows you to configure, control, and calibrate the PCI-5922. NI-SCOPE provides application programming interfaces for many development environments.

Application Software

NI-SCOPE provides programming interfaces, documentation, and examples for the following application development environments:

- LabVIEW
- LabWindows™/CVI™
- Measurement Studio
- Microsoft Visual C/C++
- .NET (C# and VB.NET)

Interactive Soft Front Panel and Configuration

When you install NI-SCOPE on a 64-bit system, you can monitor, control, and record measurements from the PCI-5922 using InstrumentStudio.

InstrumentStudio is a software-based front panel application that allows you to perform interactive measurements on several different device types in a single program.



Note InstrumentStudio is supported only on 64-bit systems. If you are using a 32-bit system, use the NI-SCOPE-specific soft front panel instead of InstrumentStudio.

Interactive control of the PCI-5922 was first available via InstrumentStudio in NI-SCOPE18.1 and via the NI-SCOPE SFP in NI-SCOPE2.2. InstrumentStudio and the NI-SCOPE SFP are included on the NI-SCOPE media.

NI Measurement & Automation Explorer (MAX) also provides interactive configuration and test tools for the PCI-5922. MAX is included on the driver media.

TClk Specifications

You can use the NI TClk synchronization method and the NI-TClk driver to align the Sample clocks on any number of supported devices, in one or more chassis. For more information about TClk synchronization, refer to the ***NI-TClk Synchronization Help***, which is located within the ***NI High-Speed Digitizers Help***. For other configurations, including multichassis systems, contact NI Technical Support at ni.com/support.

Intermodule SMC Synchronization Using NI-TClk for Identical Modules

Specifications are valid under the following conditions:

- Any number of PXI modules installed in one NI PXI-1042 chassis.
- All parameters set to identical values for each SMC-based module.
- Sample clock set to 15 MS/s and all filters disabled.

Skew ²⁶	500 ps
Average skew after manual adjustment	<10 ps

26. Caused by clock and analog path delay differences. No manual adjustment performed.

Sample clock delay/adjustment resolution	≤5 ps
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Power

Current draw	
+3.3 V DC	2.0 A
+5 V DC	2.5 A
+12 V DC	450 mA
-12 V DC	0 A

Total power	24.5 W
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Physical

Dimensions	35.5 cm × 2.0 cm × 11.3 cm (14.0 in × 0.8 in × 4.4 in)
Weight	415 g (14.6 oz)

Environment

Maximum altitude	2,000 m (at 25 °C ambient temperature)
Pollution Degree	2

Indoor use only.

Operating Environment

Ambient temperature range	0 °C to 45 °C
Relative humidity range	10% to 90%, noncondensing (Tested in accordance with IEC 60068-2-56.)

Storage Environment

Ambient temperature range	-40 °C to 71 °C (Tested in accordance with IEC 60068-2-1 and IEC 60068-2-2.)
Relative humidity range	5% to 95%, noncondensing (Tested in accordance with IEC 60068-2-56.)

Compliance and Certifications

Safety Compliance Standards

This product is designed to meet the requirements of the following electrical equipment safety standards for measurement, control, and laboratory use:

- IEC 61010-1, EN 61010-1
- UL 61010-1, CSA C22.2 No. 61010-1



Note For safety certifications, refer to the product label or the [Product Certifications and Declarations](#) section.

Electromagnetic Compatibility

This product meets the requirements of the following EMC standards for electrical equipment for measurement, control, and laboratory use:

- EN 61326-1 (IEC 61326-1): Class A emissions; Basic immunity
- EN 55011 (CISPR 11): Group 1, Class A emissions
- EN 55022 (CISPR 22): Class A emissions
- EN 55024 (CISPR 24): Immunity
- AS/NZS CISPR 11: Group 1, Class A emissions
- AS/NZS CISPR 22: Class A emissions
- FCC 47 CFR Part 15B: Class A emissions
- ICES-001: Class A emissions



Note In the United States (per FCC 47 CFR), Class A equipment is intended for use in commercial, light-industrial, and heavy-industrial locations. In Europe, Canada, Australia, and New Zealand (per CISPR 11), Class A equipment is intended for use only in heavy-industrial locations.



Note Group 1 equipment (per CISPR 11) is any industrial, scientific, or medical equipment that does not intentionally generate radio frequency energy for the treatment of material or inspection/analysis purposes.



Note For EMC declarations, certifications, and additional information, refer to the [Product Certifications and Declarations](#) section.

CE Compliance

This product meets the essential requirements of applicable European Directives, as follows:

- 2014/35/EU; Low-Voltage Directive (safety)
- 2014/30/EU; Electromagnetic Compatibility Directive (EMC)
- 2011/65/EU; Restriction of Hazardous Substances (RoHS)

Product Certifications and Declarations


Refer to the product Declaration of Conformity (DoC) for additional regulatory compliance information. To obtain product certifications and the DoC for NI products, visit ni.com/product-certifications, search by model number, and click the appropriate link.

Environmental Management


NI is committed to designing and manufacturing products in an environmentally responsible manner. NI recognizes that eliminating certain hazardous substances from our products is beneficial to the environment and to NI customers.

For additional environmental information, refer to the ***Engineering a Healthy Planet*** web page at ni.com/environment. This page contains the environmental regulations and directives with which NI complies, as well as other environmental information not included in this document.

EU and UK Customers

-  **Waste Electrical and Electronic Equipment (WEEE)**—At the end of the product life cycle, all NI products must be disposed of according to local laws and regulations. For more information about how to recycle NI products in your region, visit ni.com/environment/weee.

电子信息产品污染控制管理办法（中国RoHS）

-  **中国RoHS**—NI符合中国电子信息产品中限制使用某些有害物质指令 (RoHS)。关于NI中国RoHS合规性信息，请登录 ni.com/environment/rohs_china。(For information about China RoHS compliance, go to ni.com/environment/rohs_china.)